

(see slides)

- Permute and Point

What do we need to prove?

- Correctness:

$$\forall a, b, \text{Out}_{A,B} [A(a) \leftrightarrow B(b)] \approx \{f(a,b), f(a,b)\}$$

- Simulatability

$$\text{Sim}_B \approx \text{View}_B [A(a) \leftrightarrow B(b)]$$

$$\text{View}_B = \{b, o^i, \{e_{j \in \{0,1,2\}}^{g_i}\}, \{K^{a_i}\}, \{K^{b_i}\}, \{C_{b \in \{0,1\}}^{o_i}\}, \gamma_{OT,B}\}$$

$$\left[\begin{array}{l} \text{color}_{w_i} \in \{0,1\} \\ K_0^{w_i} \in \{0,1\}^\lambda + \text{color}_{w_i} \\ K_1^{w_i} \in \{0,1\}^\lambda + \neg \text{color}_{w_i} \end{array} \right.$$

Malicious Security

Malicious Correctness

$$\forall A, \exists D, \forall b, \text{Out}_B [A(\hat{a}) \leftrightarrow B(b)] \approx \{\hat{a} \in D: f(\hat{a}, b)\}$$

Cut-and-choose

1. Alice generate λ instances of garbled circuits

$$\{e_{i,m}^{g_i}\}_{m \in \{1, \dots, \lambda\}}, \{K_{e_{i,m}^{g_i}}^{w_i}\}_m, \{C_{b,m}^{o_i}\}_m$$

$$C_m = \text{Commit Scheme } M_{i,m} : \{K_{b,m}^{w_i}\} \rightarrow \{0,1\}$$

$$\{e\}, C_m$$

2. Alice \rightarrow $\boxed{\text{OT}}$ $\leftarrow b_i$ Run OT for every gadget circuit,

3. Bob chooses $\frac{\lambda}{2}$ circuits to open, $\mathcal{O} \subset [1, \dots, \lambda]$

4. Alice sends all the wirelabels $\{K_{\ell_0, \ell_3, m}^{w_i}\}_{m \in \mathcal{O}}$
Opens C_m for $m \in \mathcal{O}$

5. Bob checks mapping for his inputs.

6. Alice sends inputs for \mathcal{I}_θ

7. Evaluate all remaining \mathcal{I}_θ
Check that all outputs are consistent.